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EXPERIMENT 1

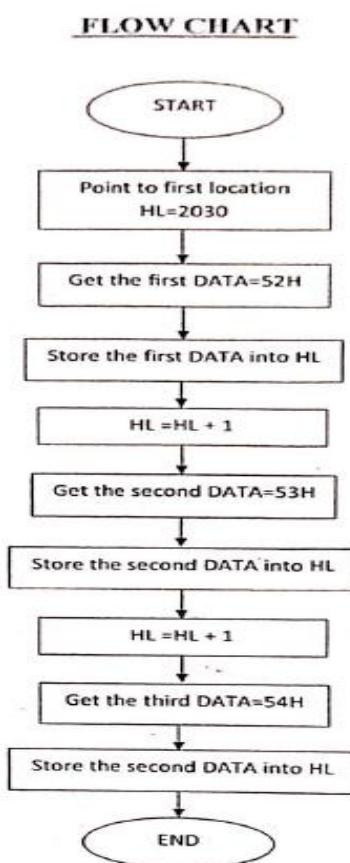
DATA TRANSFER INSTRUCTIONS

I. Introduction

These instructions move data between registers, or between memory and registers. These instructions copy data from source to destination, where the contents of source are not modified through copying. For example: MOV B, C or MOV B, M. This instruction (MOV) makes a copy to data stored in C register to B register without modified the data through copying.

II. Experiment

Store the data byte (52H, 53H, 54H) into memory location 2030, 2031, and 2032, respectively.



III. Source Code for program:

LXI H, 2030 H	<i>; point the first location in memory</i>
MVI A, 52H	<i>; Enter the data to the A register</i>
MOV M, A	<i>; Make a copy data from Memory to A register</i>
INX H	<i>; increase data of H register by 1</i>
MVI A, 53H	<i>; Enter the data to the A register</i>
MOV M, A	<i>; Make a copy data from Memory to A register</i>
INX H	<i>; increase data of H register by 1</i>
MVI A, 54H	<i>; Enter the data to the A register</i>
MOV M, A	<i>; Make a copy data from Memory to A register</i>
RST5	<i>; Finish the program</i>

INSTRUCTION CODE	REGISTER	DATA
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IV. Memory

LXI H, 2030	H,L	20, 30
MVI A,52H	Accumulator	52
MOV M,A	Memory (2030)	52



V. Simulation program

- I. Write the program code using microprocessor 8085 simulator
- II. Execute the program using this button

III. Getting result.

VI. Discussion

- 1- Write a program to exchange the content memory location 3051, 3061. Assume the content of these location.

3051 54

3051 8C

- 2- Find the machine codes and the number of bytes of the following instructions, identify the opcodes and the operands
- a) **SUB D** b) **MVI D, 8FH** C) **ADI E4H**
- 3- Specify the output of the location 3030H if the following program is executed:

MVI D, 22H

The screenshot shows the Microprocessor 8085 Simulator Software Kit 1.0 interface. On the left, the assembly code window displays the following program:

```
LXI H 2030h  
MVI A 52h  
MOV M A  
INX H  
MVI A 53h  
MOV M A  
INX H  
MVI A 54h  
MOV M A  
RST 5
```

In the center, the "program Result" window shows the memory dump starting at address 2030:

Address	Data
2030	52
2031	53
2032	54
2033	
2034	
2035	
2036	
2037	
2038	
2039	
203A	
203B	
203C	
203D	
203E	

A circular selection highlights the row for address 2032, which contains the value 54. To the right of the memory dump are several instruction group tables:

- 1. Data Transfer Group: MOV, MVI, LDA, STA, LHLD, SHLD, LXI, XCHG, LDAX, STAX, IN, OUT
- 2. Arithmetic Group: ADD, ADI, ADC, ACI, SUB, SUI, SBD, SBI, INR, DCR, INV, DCX, DAA, DAD
- 3. Logical Group: ANA, ANI, XRA, XRI, CMA, ORA, ORI, CMP, CPI, CMC, RLC, RRC, RAL, RAR, STC
- 4. Branch and Machine Control Group: JMP, J_-, CALL, C_, RET, R_-, EI, DI, PUSH, PCHL, RST, RIM, SIM, POP, XTHL, SPHL, NOP, HLT

At the bottom, there is an "Instruction Information" section with checkboxes for "Auto Insert" and "Insert" / "Cancel".

MOV A, D

MOV B, A

MVI C, A5H

STA 3030H

4- Specify the content of the register and the flag status as the following instruction are executed (A,B,C,D,S,Z,CY)

MVI A, 00H

MVI B, 85H

MOV C, A

MOV D, B

ADD B

SUB C

RST5