

COLLEGE OF ENGINEERING AND TECHNOLOGIES ALMUSTAQBAL UNIVERSITY

Electronics CTE 207

Lecture 10

- Full-Wave Rectifier (Center-Tapped) -(2023 - 2024) Dr. Zaidoon AL-Shammari Lecturer / Researcher

zaidoon.waleed@mustaqbal-college.edu.iq

www.uomus.edu.iq



The result of full-wave rectification is a DC output voltage that pulsates every half-cycle of the input, as shown in Figure below.



The average value for a full-wave rectified output voltage is twice that of the half-wave rectified output voltage, expressed as follows:

$$V_{AVG} = \frac{2V_{p(out)}}{\pi}$$



The difference between half-wave and full-wave rectification is that:

➤ Half-wave rectifier:

Allows only one-half of the current to the load during the entire input cycle.

$$\begin{array}{c|c} V_{out} \\ 0 \\ t_0 \end{array} \begin{array}{c} t_1 \\ t_2 \end{array} \begin{array}{c} t_3 \\ t_3 \end{array} \begin{array}{c} t_4 \\ t_5 \end{array}$$

➢ Full-wave rectifier :

Allows unidirectional current to the load during the entire input cycle.

$$V_{out} \qquad 0 \qquad t_0 \qquad t_1 \qquad t_2 \qquad t_3 \qquad t_4 \qquad t_5$$



Full-Wave Bridge Rectifier







- The center-tapped (CT) full-wave rectifier uses two diodes connected to the secondary of a center-tapped transformer.
- \succ The input signal is coupled through the transformer to the secondary.
- Half of the secondary voltage appears between the center tap and each end of the secondary winding.



For a positive half-cycle of the input voltage, the polarities of the secondary voltages are shown in Figure below.

- a) Forward-biases the upper diode D1.
- b) Reverse-biases the lower diode D2.

The current path is through D1 and the load resistor RL, as indicated.



For a negative half-cycle of the input voltage, the polarities of the secondary voltages are shown in Figure below.

- a) Forward-biases the upper diode D2.
- b) Reverse-biases the lower diode D1.

The current path is through D2 and the load resistor RL, as indicated.

Positive – Negative cycle





Dr. Zaidoon AL-Shammari

Computer Techniques Engineering



- \succ The output voltage is determined by the turns ratio, n, of the transformer.
- > The peak output voltage is one-half the peak secondary voltage.
- > The primary voltage Vpri is the same as the input voltage Vin.



Example



To obtain an output voltage Vp(out) with a peak value approximately equal to the input peak Vp(in), what would be the turn ratio n of a transformer?

Sol: $V_{p(out)} = \frac{nV_{p(in)}}{2}$ $n = \frac{2V_{p(out)}}{V_{p(in)}}$

Since, $V_{p(out)}$ equal $V_{p(in)}$

n = 2



Example



Specify the turns ratio of a transformer required for a center-tapped full wave rectifier if the input voltage is 311 V and the required output is 12 V peak?

Sol:

 $n = \frac{2V_{p(out)}}{V_{p(in)}}$ $n = \frac{2 \times 12}{311} = 0.0771$



Example



For a center-tapped full-wave rectifier if the input voltage is 311 V, What is the peak output if the turns ratio is 0.15?



Peak Inverse Voltage (PIV)



- ➤ Each diode in the FWR is alternately forward-biased and then reversebiased.
- The maximum reverse voltage VR that each diode must withstand is the peak value of the total secondary voltage Vp(sec).



AL- MUSTAQBAL UNIVERSITY COMPUTER TECHNIQUES ENGINEERING





