



LECTURE 5

Full-Wave Rectifier (FWR)

Analog Electronics

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Outline and Aim

After completing this lecture, you should be able to:

- Analyze the operation of a full-wave rectifier
- Describe how the diodes function in a rectifier
- Determine the average value of a full-wave rectified voltage
- Describe the Center-Tapped Full-Wave Rectifier
- Describe the effect of the Turns Ratio *n* on Full-Wave Output Voltage
- Determine the peak inverse voltage (PIV)

The result of full-wave rectification is a dc output voltage that pulsates every halfcycle of the input, as shown in Fig. 9.



Fig. 9: Full-wave rectification

The average value for a full-wave rectified output voltage is twice that of the half-wave rectified output voltage, expressed as follows:

$$V_{AVG} = \frac{2V_{p(out)}}{\pi}$$

The **difference** between half-wave and full-wave rectification is that:

Half-wave rectifier:

Allows only one-half of the current to the load during the entire input cycle.

$$V_{out} \qquad 0 \qquad t_1 \qquad t_2 \qquad t_3 \qquad t_4 \qquad t_5$$

Fig. 10: Output of a half-wave rectifier

$$V_{AVG} = \frac{V_{p(out)}}{\pi}$$

Full-wave rectifier :

Allows unidirectional current to the

load during the entire input cycle.



Fig. 11: Output of a full-wave rectifier

$$V_{AVG} = \frac{2V_{p(out)}}{\pi}$$

EXAMPLE 5:

Find the average value of the full-wave rectified output voltage in Fig. 12



Fig. 12: Output of a half-wave rectifier

$$V_{AVG} = \frac{2V_{p(out)}}{\pi} = \frac{2*15}{3.14} = 9.55 V$$

1. Center-Tapped Full-Wave Rectifier

Fig. 13: Center-Tapped Full-Wave Rectifier



2. Full-Wave Bridge Rectifier

Fig. 14: Full-Wave Bridge Rectifier



1. Center-Tapped Full-Wave Rectifier

- The center-tapped (CT) full-wave rectifier uses two diodes connected to the secondary of a center-tapped transformer.
- The input signal is coupled through the transformer to the secondary.
- Half of the secondary voltage appears between the center tap and each end of the secondary winding, as shown in Fig. 13.



Fig. 13: Center-Tapped Full-Wave Rectifier

1. Center-Tapped Full-Wave Rectifier

For a **positive half-cycle** of the input voltage, the polarities of the secondary voltages are shown in Fig 15.

a) Forward-biases the upper diode D_1 b) Reverse-biases the lower diode D_2 .

The current path is through D_1 and the load resistor R_L , as indicated.



Fig. 15: During positive half-cycles, D_1 is forward-biased and D_2 is reverse-biased.

1. Center-Tapped Full-Wave Rectifier

For a **negative half-cycle** of the input voltage, the polarities of the secondary voltages are shown in Fig 15.

a) Forward-biases the lower diode D_2 b) Reverse-biases the upper diode D_1 .

The current path is through D_2 and the load resistor R_L , as indicated.



Fig. 15: During negative half-cycles, D_2 is forward-biased and D_1 is reverse-biased.

Effect of the Turns Ratio *n* **on Full-Wave Output Voltage:**

- The output voltage is determined by the **turns ratio**, *n*, of the transformer.
- The peak output voltage is one-half the peak secondary voltage.



Example1: To obtain an output voltage $V_{p(out)}$ with a peak value approximately equal to the input peak $V_{p(in)}$, what would be the turn ratio *n* of a transformer?



n = 2

Example2: Specify the turns ratio of a transformer required for a center-tapped full-wave rectifier if the input voltage is $220 V_{rms}$ and the required output is 12 V peak?



Example3: For a center-tapped full-wave rectifier if the input voltage is $220 V_{rms}$, What is the peak output if the turns ratio is 0.15?





$$V_{p(out)} = \frac{0.15 \times 311}{2} = 23.3 V$$

Peak Inverse Voltage (PIV)

Each diode in the FWR is alternately forward-biased and then reverse-biased. **The maximum reverse voltage** V_R that each diode must withstand is the peak value of the total secondary voltage $V_{p(sec)}$.



Fig. 17: Center-Tapped Full-Wave Rectifier showing the Peak Inverse Voltage PIV

Peak Inverse Voltage (PIV)

When the total secondary voltage has the polarity shown in Fig. 17, The anode of D₁ is $+\frac{V_{p(sec)}}{2}$ Which is equal to the cathode voltage The anode of D_2 is $-\frac{V_{p(sec)}}{2}$ $V_{D2} = \frac{V_{p(sec)}}{2} - \frac{-V_{p(sec)}}{2}$ D_1 V_{in} 'sec $V_{D2} = V_{p(sec)}$ D_{γ} $PIV = V_{p(sec)}$ $V_{p(sec)}$

 $PIV = 2V_{p(out)}$

Fig. 17: Center-Tapped Full-Wave Rectifier showing the Peak Inverse Voltage PIV

Example 4:

a) For ideal diodes, show the voltage waveforms across the secondary winding and R_L when a 120 V_{rms} sine wave is applied to the primary winding in Fig. 18.
b) What minimum PIV rating must the diodes have?

Solution:

a)
$$V_{p(pri)} = \frac{V_{rms}}{0.707} = 169.7 V$$

$$V_{p(sec)} = nV_{p(pri)}$$

 $V_{p(sec)} = 2V_{p(pri)} = 2 \times 169.7$ = 339.4 V



Fig. 18: Center-Tapped Full-Wave Rectifier

$$V_{p(RL)} = \frac{V_{p(sec)}}{2} = \frac{339.4}{2} = 169.7 V$$

Complement example 4:



Waveforms across the secondary winding



b)
$$PIV = V_{p(sec)} = 339.4 V$$

Example 5:

a) For the diodes, show the voltage waveforms across the secondary winding and R_L when a 120 V_{rms} sine wave is applied to the primary winding in Fig. 18.
b) What minimum PIV rating must the diodes have?



Fig. 18: Center-Tapped Full-Wave Rectifier