

AL- MUSTAQBAL UNIVERSITY COLLEGE DEPARTMENT OF BIOMEDICAL ENGINEERING

Bio-Electronics Design Lab BME 515

Lecture 5

- Electrocardiogram II -

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Lecturer / Researcher

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Electrocardiogram (ECG)





- 1. Power supplies.
- 2. Block Diagrams.
- 3. Schematic Diagrams.
- 4. Circuit Diagrams.





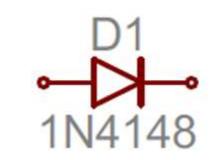
How to Read a Schematic

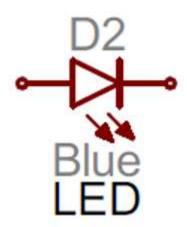


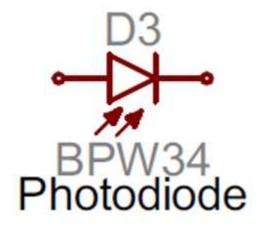
- Diodes.
- Transistors.
- Digital Logic Gates.
- Integrated Circuits.
- Op Amps.
- Headers and Connectors.
- Transformers.
- Speakers.
- Relays.

Diodes





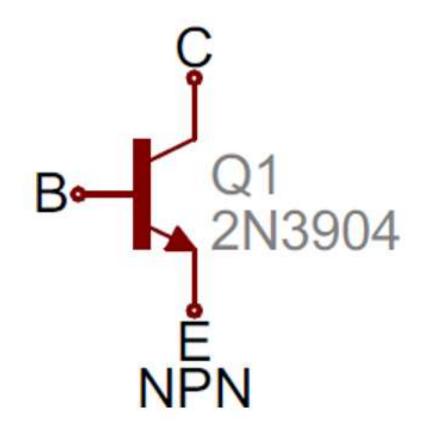


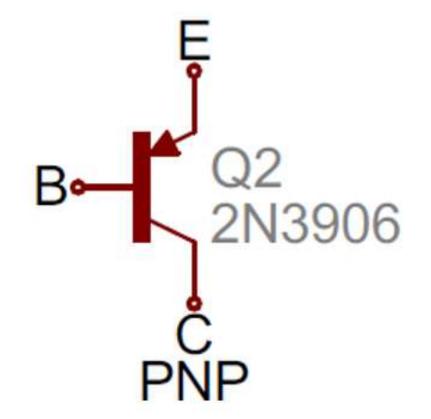


Transistors





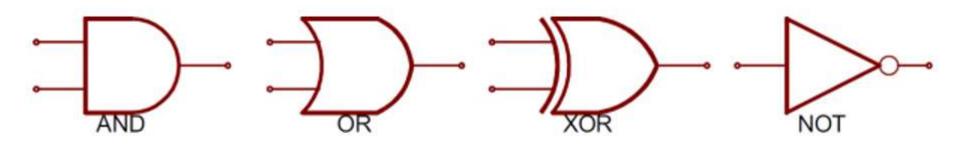




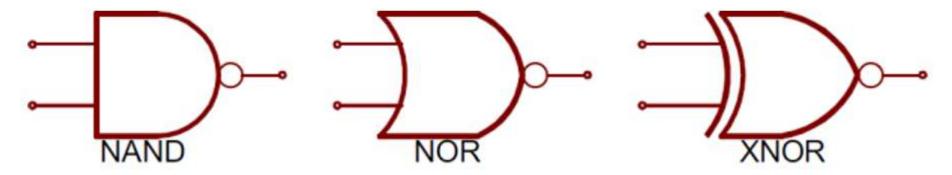






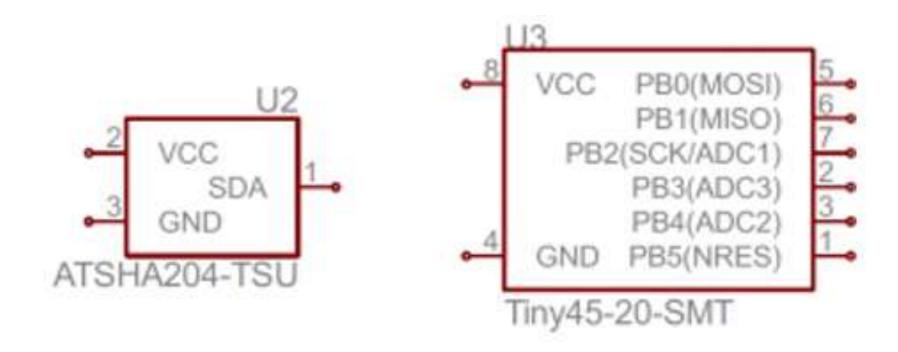


Adding a bubble to the output negates the function, creating NANDs, NORs, and XNORs:



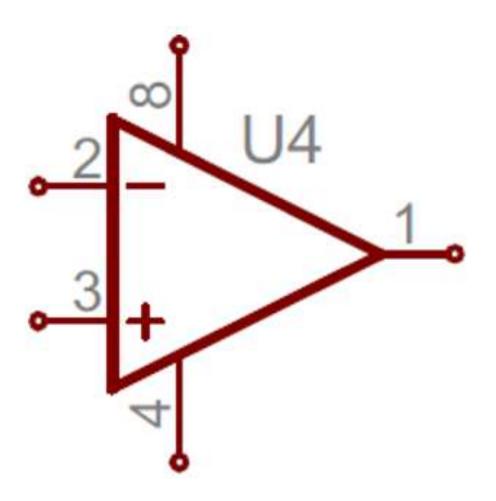
Integrated Circuits





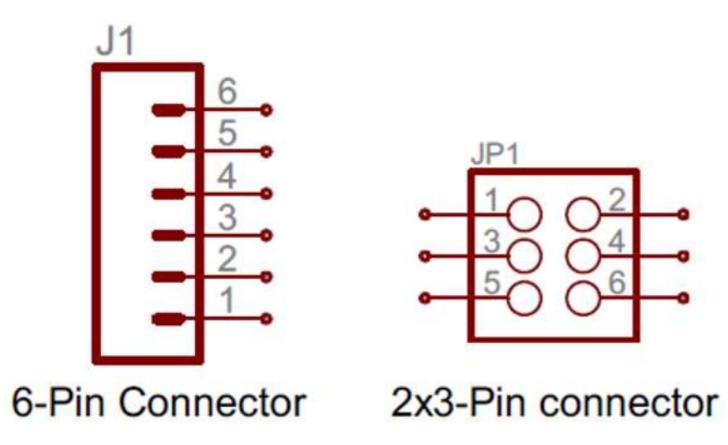
Operation Amplifiers (Op Amps)





Headers and Connectors

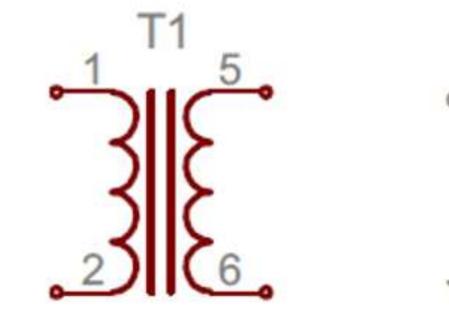


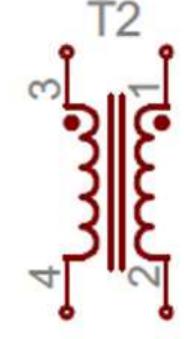


Transformers





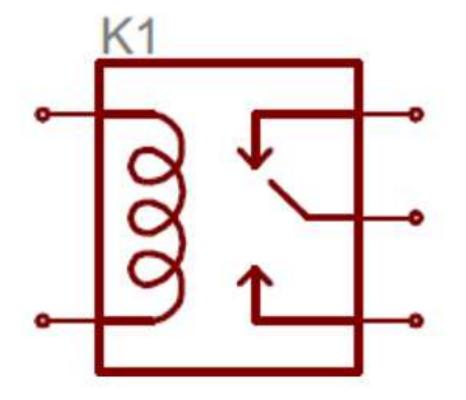






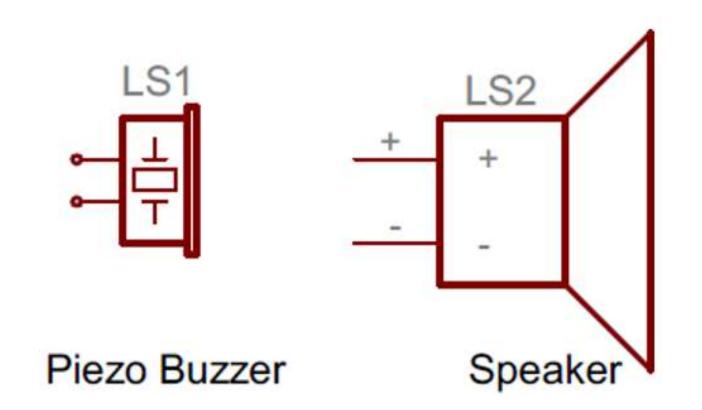






Speakers and buzzers

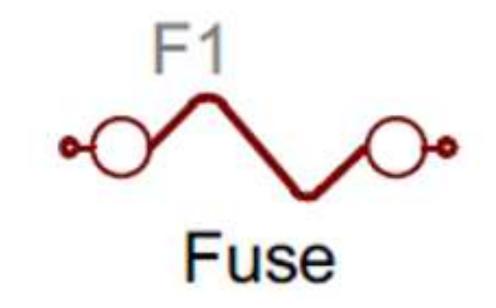












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