



AL MUSTAQBAL UNIVERSITY COLLEGE



STUDENT NAME:			
TUTOR NAME:	Dr. Ameer Al-khaykan		
PROGRAMME:	Electrical Circuit		
SUBJECT:	Electrical and Electronics		
COURSEWORK TITLE:	superposition theorem		

Issue Date:	Due Date:	Feedback Date:	Extension Date:
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PERFORMANCE

TARGETED LEARNING OUTCOMES

4. Solve problems involving basic analogue and digital electronic circuits using numerical skills appropriate to an engineer.
5. Identify and safely use standard laboratory equipment to extract data, then apply in the solution of an electronic or electrical engineering problem;
6. Adopt a logical approach to the solution of engineering problems.

Important Information – Please Read Before Completing Your Work

All students should submit their work by the date specified using the procedures specified in the Student Handbook. An assessment that has been handed in after this deadline will be marked initially as if it had been handed in on time, but the Board of Examiners will normally apply a lateness penalty.

Your attention is drawn to the Section on Academic Misconduct in the Student's Handbook.

All work will be considered as individual unless collaboration is specifically requested, in which case this should be explicitly acknowledged by the student within their submitted material.

Any queries that you may have on the requirements of this assessment should be e-mailed to **ameer.alkhaykan@mustaqbal-college.edu.iq**

No queries will be answered after respective submission dates.

You must ensure you retain a copy of your completed work prior to submission.

MARKING CRITERIA

COURSEWORK WILL BE MARKED ACCORDING TO THE FOLLOWING UNIVERSITY CRITERIA.

90-100%: a range of marks consistent with a first where the work is exceptional in all areas;

80-89%: a range of marks consistent with a first where the work is exceptional in most areas.

70-79%: a range of marks consistent with a first. Work which shows excellent content, organisation and presentation, reasoning and originality; evidence of independent reading and thinking and a clear and authoritative grasp of theoretical positions; ability to sustain an argument, to think analytically and/or critically and to synthesise material effectively.

60-69%: a range of marks consistent with an upper second. Well-organised and lucid coverage of the main points in an answer; intelligent interpretation and confident use of evidence, examples and references; clear evidence of critical judgement in selecting, ordering and analysing content; demonstrates some ability to synthesise material and to construct responses, which reveal insight and may offer some originality.

50-59%: a range of marks consistent with lower second; shows a grasp of the main issues and uses relevant materials in a generally business-like approach, restricted evidence of additional reading; possible unevenness in structure of answers and failure to understand the more subtle points: some critical analysis and a modest degree of insight should be present.

40-49%: a range of marks which is consistent with third class; demonstrates limited understanding with no enrichment of the basic course material presented in classes; superficial lines of argument and muddled presentation; little or no attempt to relate issues to a broader framework; lower end of the range equates to a minimum falls short in one or more areas.

35-39%: achieves many of the learning outcomes required for a mark of 40% but falls short in one or more areas.

30-34%: a fail; may achieve some learning outcomes but falls short in most areas; shows considerable lack of understanding of basic course material and little evidence of research.

0-29%: a fail; basic factual errors of considerable magnitude showing little understanding of basic course material; falls substantially short of the learning outcomes for compensation.

Note:

- While constructing circuits all connects should be made with the power supply in the off position.
- Check power and ground connections (and other connections) **before** switch on the power.
- Make sure that the power and the ground are properly connected to all IC's before switch on the power.
- **DO NOT** strip wire ends longer than 1/4" and jam long bare ends into the breadboard holes. This will cause shorts and ruin the board.
- **DO NOT** short (connect) the power supply outputs together, i.e., do not allow the exposed wires to touch each other. This will cause permanent damage to the power supply.
- **DO NOT** connect the power supply to the breadboard with reverse polarity. This will cause the permanent chip damage.
- **DO NOT** connect an output of any gate to the output of another gate, to a switch, to power (+5V), or to ground. These situations will cause excessive currents and result in the permanent damage to the chip or chips involved.

4.1. Object:

- To acquire the knowledge of implementing the superposition theorem as a tool in performing network analysis verify it practically.

4.2. Apparatus:

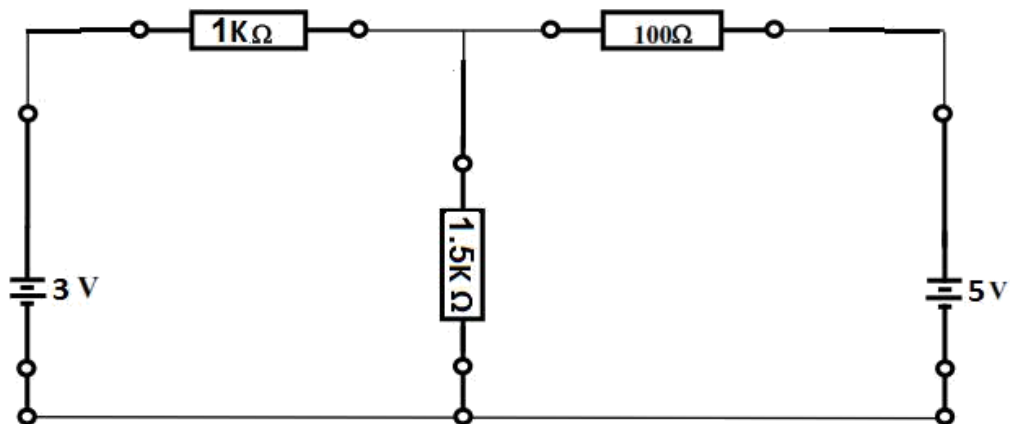
- Resistors of different ratings.
- D.C power supply.
- Measuring instruments (Voltmeter, Ammeter and Ohmmeter).
- Connecting wires and board.

4.3. Theory:

In a linear network containing more than one source (current or voltage) the resultant current and voltage in any branch is the algebraic sum of currents and voltages that would be produced by each source, acting alone, all the other sources, being replaced meanwhile by their respective internal resistances.

4.4. Procedure:

1. Connect the circuit shown below.



- Measure the Voltage across ($1.5\text{ k}\Omega$) resistor when the two sources are connected
- Measure the Voltage across ($1.5\text{ k}\Omega$) resistor , when :
 - The source of (3 v) is ON and The source of (5 v) is OFF
 - The source of (5 v) is ON and The source of (3 v) is OFF
- Apply the algebraic sum of the results in step (2) to calculate the amount of voltage across ($1.5\text{ k}\Omega$) resistor.

4.5. Discussion:

1. What is the condition under which the superposition theorem can be applied?
2. Define the Superposition theorem and why we consider it the simplest theorem?

4.5. Homework

1. For the circuit shown in Fig calculate the current through the $1.5\text{k}\Omega$ resistor using superposition theorem.
2. For the circuit shown in Fig , determine the current I . Is the reciprocity theorem satisfied?